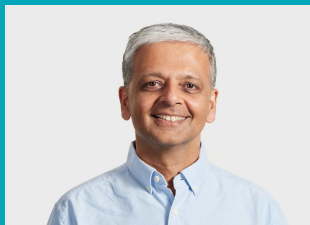


Rivos Inc.

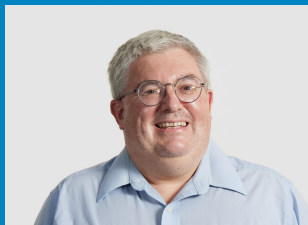
美商睿智科技

Meet the Leadership Team



Puneet Kumar
Founder, CEO

Formerly Director @
Google Chromebook
Software, Agnilux, Apple,
PA Semi, Broadcom, SiByte



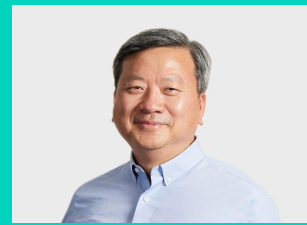
Mark Hayter
Founder, CSO

Formerly Director @
Google Chrome Hardware.
Agnilux, Apple, PA Semi,
Broadcom, SiByte



Belli Kuttanna
Founder, CTO

Formerly Intel Senior
Fellow & CTO @ Intel
Capital. Chief architect @
Intel, VP @ Qualcomm



Tse-Yu Yeh
VP Eng (Silicon)

Formerly Senior Director of
CPU Design @ Apple, PA
Semi, Broadcom, SiByte

We have >400 employees on board building teams across SW, HW and Chip Design. People have joined us from companies such as Google, Intel, Apple, Qualcomm, AMD, Meta and other Startups!

Global Team



Rivos is headquartered in Santa Clara, CA with other US offices in Austin, TX, Fort Collins, CO, Portland, OR and also some remote employees.

We have 3 international sites in Bangalore, IN, Cambridge, UK, and Hsin-Chu, TW.

In office locations we support a flexible/hybrid work arrangement.

2021

Founded

07

Offices

Headquarters

Santa Clara, CA

Product Overview

Building high performance silicon systems and software to address the growing compute demands of Generative AI and data analytics, through a full solution of optimized chips combining:

- ✓ 64 bit RISC-V CPUs
- ✓ Data Parallel Accelerator(DPA) - a GPGPU optimized for large language models and data analytics
- ✓ Tightly integration memory system between the CPU and DPA enabling uniform memory across DDR DRAM and HBM driven by the terabytes of memory requirements for models and databases
- ✓ Reference multi-chip Open Compute Project (OCP) modular server
- ✓ Full firmware-to-application open software stack
 - UEFI and Linux base supports standard frameworks (such as PyTorch including eager mode, JAX, Spark, and PostgreSQL).
 - Allowing applications and models to be recompiled and not needing redesign

Enabling users to seamlessly deploy Rivos products and immediately start benefiting from best-in-class performance for time-to-insights, power efficiency for market leading perf/watt, throughput, and reduced total cost of ownership.

Rivos engages with the open source community

Open Source and Community sharing are important to innovation

“Use Open Source wherever we can and contribute back. I expect we will use Open Source software extensively in our company and contribute back to technologies like bootloaders, kernels, middleware, tools, simulators, emulators etc. I also expect the full software stack for our reference designs to be open source.”

– Puneet Kumar, Rivos CEO

Hardware & Security

RISC-V ISA and System Standards

OpenTitan Comportable IP



Software & Security

Linux, GCC, LLVM, QEMU, Spike, minijail



Rivos is Hiring!!



At Rivos we believe the team defines the company and its products. We value our diverse backgrounds and perspectives and encourage everyone to bring their genuine selves to work with no inhibitions or fears. At Rivos we also believe that everyone has a voice and is heard.

Internship candidates for SW, HW, and Chip designs!
Please apply for jobs through Lever at jobs.lever.co/rivosinc

Rivos Taiwan 2025 Internships

Internship positions are available to qualified students

- 3 to 6-month internship
- 20 or 40 hours/week
- Master or PHD students graduated in 2026
- Rivos Taiwan office location: Five minutes walking distance from HsinChu HSR
- Hybrid work model

Positions are available in the following areas:

- Physical Design
- BackEnd CAD
- DPA Performance



If you liked to learn more about Rivos
go to www.rivosinc.com

Please feel free to apply through Lever at
jobs.lever.co/rivosinc.

